

DOCKET NO. P05655 (NATI15-05655)
Customer No. 13990

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : Javendar Rajagopalan, et al.
U.S. Serial No. : 10/654,318
Filed : September 3, 2003
For : DISCRETE-TIME AMPLITUDE CONTROL OF VOLTAGE-
CONTROLLED OSCILLATOR
Group No. : 2817
Examiner : (Not Yet Assigned)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

Sir:

The undersigned hereby certifies that the following documents:

1. Information Disclosure Statement;
2. Form PTO/SB/08B;
3. Two (2) references as cited on Information Disclosure Statement; and
4. A postcard receipt;

relating to the above application, were deposited as "First Class Mail" with the United States Postal Service, addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on Feb 2, 2004

Date: Feb 2, 2004

Kathy Hamilton
Mailer

Date: Jan 30, 2004

William A. Munck
William A. Munck
Reg. No. 39,308

P.O. Drawer 800889
Dallas, Texas 75380
Phone: (972) 628-3600
Fax: (972) 628-3616
E-mail: wmunck@davismunck.com



DOCKET NO. P05655 (NATI15-05655)
Customer No. 23990

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : Javendar Rajagopalan, et al.
U.S. Serial No. : 10/654,318
Filed : September 3, 2003
For : DISCRETE-TIME AMPLITUDE CONTROL OF VOLTAGE-
CONTROLLED OSCILLATOR
Group No. : 2817
Examiner : (Not Yet Assigned)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO/SB/08. Copies of the listed references are submitted herewith.

Publications

MARGARIT, Mihai A., et al, "A Low-Noise, Low-Power VCO With Automatic Amplitude Control for Wireless Applications," IEEE Journal of Solid-State Circuits, Vol. 34, No. 6, June 1999, pp. 761-771.


ROGERS, John W.M., et al., "A Study of Digital and Analog Automatic-Amplitude Control Circuitry for Voltage-Controlled Oscillators," IEEE Journal of Solid-State Circuits, Vol. 38, No. 2, February 2003, pp. 352-356.

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

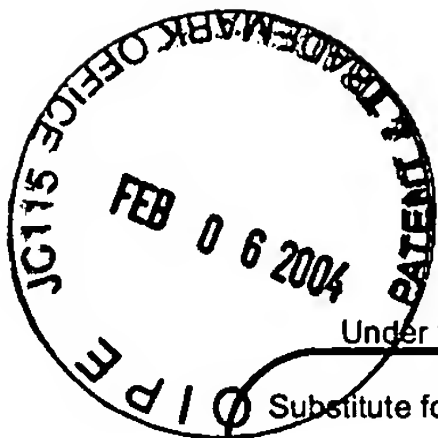
DAVIS MUNCK, P.C.

Date: Jan. 30, 2004



William A. Munck
Registration No. 39,308

P.O. Drawer 800889
Dallas, Texas 75380
Phone: (972) 628-3600
Fax: (972) 628-3616
email: wmunck@davismunck.com



PTO/SB/08B (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449/PTO

Complete if Known**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Application Number

10/654,318

Filing Date

September 3, 2003

First Named Inventor

Javendar Rajagopalan

Art Unit

2817

Examiner Name

(Not Yet Assigned)

Sheet

1

of

1

Attorney Docket Number

P05655

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AA	MARGARIT, Mihai A., et al, "A Low-Noise, Low-Power VCO With Automatic Amplitude Control for Wireless Applications," IEEE Journal of Solid-State Circuits, Vol. 34, No. 6, June 1999, pp. 761-771.	
	AB	ROGERS, John W.M., et al., "A Study of Digital and Analog Automatic-Amplitude Control Circuitry for Voltage-Controlled Oscillators," IEEE Journal of Solid-State Circuits, Vol. 38, No. 2, February 2003, pp. 352-356.	

**Examiner
Signature****Date****Considered**

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.